

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANTS  
PTO-1449**

Attorney Docket No. 2885/96	Serial No. 10/551,891
Applicant(s) <b>VORBACH</b>	
Filing Date <b>August 28, 2006</b>	Group Art Unit <b>2183</b>

**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT/PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE
	4,623,997	November 18, 1986	Tulpule			
	5,212,777	May 18, 1993	Gove et al.			
	5,572,710	November 5, 1996	Asano et al.			
	5,606,698	February 25, 1997	Powell			
	5,659,785	August 19, 1997	Pechanek et al.			
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	5,804,986	September 8, 1998	Jones			
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	6,434,672	August 13, 2002	Gaither			
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EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	3-961028	August 15, 2007	Japan			Abstract	
	2001-510650	July 31, 2001	Japan			Abstract only	
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**OTHER DOCUMENTS**

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	Epstein, Dave, "IBM Extends DSP Performance with Mfxx," Microprocessor Report, Vol. 9, No. 16 (MicroDesign Resources), December 4, 1995, pp. 1-4 [XL0029013].
	Galantis, M.D. et al., "Accelerating Applications by Mapping Critical Kernels on Coarse-Grain Reconfigurable Hardware in Hybrid Systems," Proceedings of the 13 <sup>th</sup> Annual IEEE Symposium on Field-Programmable Custom Computing Machines, 2005, 2 pages.
	Guo, Z. et al., "A Compiler Intermediate Representation for Reconfigurable Fabrics," University of California, Riverside, Dept. of Electrical Engineering, IEEE 2006, 4 pages.
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EXAMINER	/Keith Vicary/
	DATE CONSIDERED
	10/16/2010

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.